

SSCL19P60GT4

P-Channel Enhancement Mode MOSFET

> Features

V _{DS}	V _{GS}	R _{DS(ON)} Typ.	l _D
-60V	±20V	19mΩ@-10V	-67A
	<u> </u>	25mΩ@-4.5V	-077

Description

This device is P-Channel enhancement MOSFET.

Uses SGT technology and design to provide excellent

RDSON with low gate charge. This device is suitable
for use in DC-DC conversion, power switch and
charging circuit.

100% UIS + ΔVDS + Rg Tested!

Applications

- Load Switch
- PWM Application
- Power Management
- DC/DC Conversion

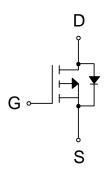
> Ordering Information

Device	Package	Shipping
SSCL19P60GT4	TO-220-3L	50/Tube

Pin configuration



TO-220-3L (Top View)



Pin Configuration



(XXYY: Internal Traceability Code)



➤ Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit	
V_{DSS}	Drain-to-Source Volta	Drain-to-Source Voltage		V
V _{GSS}	Gate-to-Source Volta	Gate-to-Source Voltage		V
	Outine Dair Outli	T _C =25℃	-67	^
l _D	Continuous Drain Current ^d	T _C =100℃	-42	Α
		T _A =25℃	-10	Δ.
IDSM	Continuous Drain Current ^a	T _A =70°C	-6.5	- A
I _{DM}	Pulsed Drain Curren	Pulsed Drain Current b		Α
Б	Power Dissipation °	Tc=25℃	96	W
P _D		T _C =100℃	38	
	Power Dissipation ^a	T _A =25℃	2.3	W
P _{DSM}		T _A =70°C	0.9	
I _{AS}	Avalanche Current ^b L=0.5mH Single Pulse		-20	Α
Eas	Avalanche Energy ^b L=0.5mH Single Pulse		100	mJ
TJ	Operation junction temperature		-55~150	°C
T _{STG}	Storage temperature ra	-55~150	$^{\circ}$	

➤ Thermal Resistance Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Max.	Unit
ReJA	Junction-to-Ambient Thermal Resistance ^a	55	65	°C/W
$R_{ heta JC}$	Junction-to-Case Thermal Resistance	1.3	1.7	C/VV

Note:

- a. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with T_A=25°C. The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P_D is based on T_{J(MAX)}=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- d. The maximum current rating is package limited.

SSC-V1.0 www.sscsemi.com Analog Future



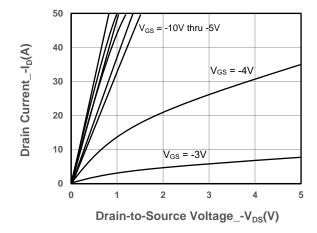
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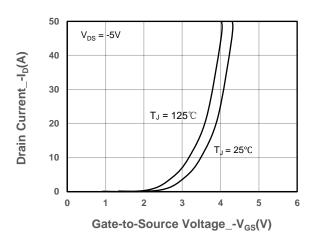
\succ Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_D = -250\mu A$	-60			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = -250uA$	-1	-1.8	-2.5	V
Drain Course On Besistance	D	V _{GS} = -10V, I _D = -20A		19	26	- mΩ
Drain-Source On-Resistance	R _{DS(on)} V _{GS}	V _{GS} = -4.5V, I _D = -10A		25	33	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -60V, V _{GS} = 0V			-1	μA
Gate-Source Leak Current	Igss	V _{GS} = ±20V, V _{DS} = 0V			±100	nA
Transconductance	G _{FS}	V _{DS} = -5V, I _D = -10A		18		S
Forward Voltage	V _{SD}	V _{GS} = 0V, I _S = -10A		-0.8	-1.3	V
Gate Resistance	R _G	V _{DS} = 0V, f = 1MHz		7.8		Ω
Input Capacitance	Ciss	V = 20V V = 0V		1510		
Output Capacitance	Coss	$V_{DS} = -30V, V_{GS} = 0V,$		250		pF
Reverse Transfer Capacitance	Crss	f = 1MHz		15		
Total Gate Charge	Q _G	10/1/		23		
Gate to Source Charge	Q _{GS}	$V_{GS} = -10V, V_{DS} = -30V,$		4		nC
Gate to Drain Charge	Q _{GD}	I _D = -20A		3.5		
Turn-on Delay Time	T _{D(ON)}			14		
Rise Time	Tr	V _{GS} = -10V, V _{DS} = -30V,		16		
Turn-off Delay Time	T _{D(OFF)}	$I_D = -20A, R_G = 3\Omega,$		41		ns
Fall Time	T _f			46		
Diode Recovery Time	Trr	I _F =-20A, di/dt=100A/us		62		ns
Diode Recovery Charge	Q _{rr}	I _F =-20A, di/dt=100A/us		107		nC

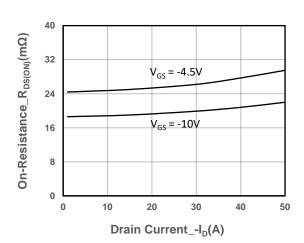


➤ Typical Performance Characteristics (T_A=25°C unless otherwise noted)

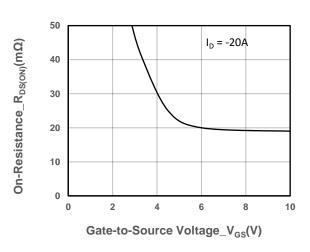




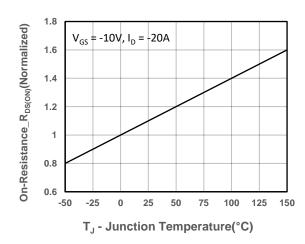
Output Characteristics



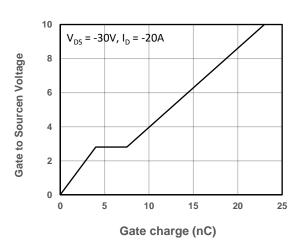
Transfer Characteristics



On-Resistance vs. Drain Current and Gate Voltage



On-Resistance vs. Gate-to-Source Voltage

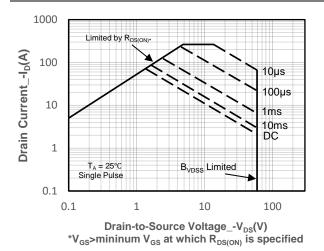


On-Resistance vs. Junction Temperature

Gate-Source Voltage vs. Gate charge

4 / 7

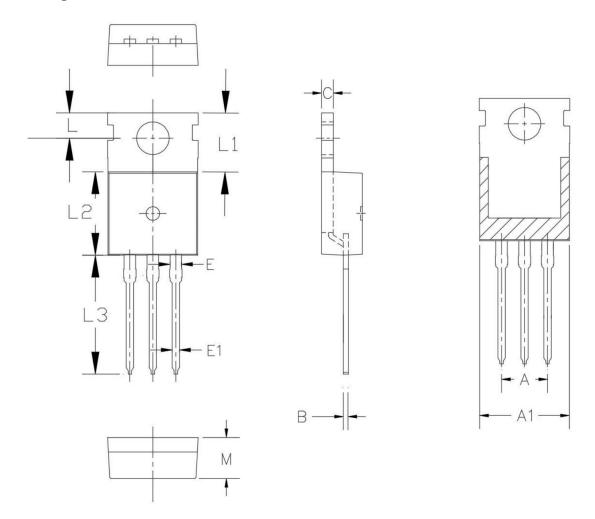




Safe Operating Area vs. Junction-to-Ambient



Package Information



Symbol	MILL IMETER			
Symbol	Min	Nom	Max	
Α	5.08 BSC			
A1	9.00	10.00	11.00	
В	0.33		0.65	
С	1.20		1.40	
E	1.17		1.37	
E1	0.60		1.10	
L	2.50		3.00	
L1	6.3	6.5	6.7	
L2	8.95		9.75	
L3	12.88		13.40	
M	4.30		4.70	



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